

# 650V 59mohm Super-Junction Power MOSFET

## AK2S65N590WMF

### Description:

This SJ device integrated with fast-recovery diode provides good FOM factor, EMI Friendly for customer application.

### Features:

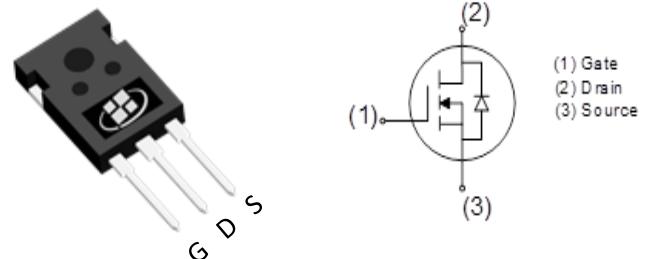
- RoHS compliant
- Halogen-free
- 100% UIS tested
- EMI-Friendly

### Applications:

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LED Light

### Key Performance Parameters:

Parameter	Value	Unit
$V_{DS}$	650	V
$R_{DS(ON)}$ , max @ $V_{GS} = 10$ V	59	mΩ
$I_D$	50	A



### Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AK2S65N590WMF	TO-247-3L	2S65N590WMF	Tube	300 per box

**Maximum Ratings** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain - Source Voltage	650	V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) <sup>(Note 1)</sup>	50	A
	Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ )	32	A
$I_{DM}$	Drain Current - Pulsed <sup>(Note 2)</sup>	200	A
$V_{GS}$	Gate - Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(Note 3)</sup>	1390	mJ
dv/dt	MOSFET dv/dt ruggedness	100	V/ns
	Reverse diode dv/dt	35	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ )	403	W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	°C

**Thermal Characteristics**

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction - to - Case, Steady-State	0.31	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction - to - Ambient, Steady State <sup>(Note 4)</sup>	35	°C/W

**Notes:**

1. The max drain current rating limited by package and maximum junction temperature
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3.  $L = 23 \text{ mH}$ ,  $V_{DD} = 150 \text{ V}$ ,  $I_{AS} = 11 \text{ A}$ ,  $R_g = 50 \Omega$ , Starting  $T_J = 25^\circ\text{C}$ , guarantee by design
4. Mount on minimum PCB layout

**Electrical Characteristics** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Static Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain - Source Breakdown Voltage	$V_{GS} = 0 \text{ V}$ , $I_D = 1 \text{ mA}$	650			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = 650 \text{ V}$ , $V_{GS} = 0 \text{ V}$			10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate Leakage Current	$V_{GS} = \pm 30 \text{ V}$ , $V_{DS} = 0 \text{ V}$			$\pm 100$	nA
$V_{GS(\text{th})}$	Gate Threshold voltage	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	3	3.5	5	V
$R_{\text{DS(ON)}}$	Drain - Source on - state resistance	$V_{GS} = 10 \text{ V}$ , $I_D = 20 \text{ A}$		48	59	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{DS} = 400 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $f = 1 \text{ MHz}$		5270		pF
$C_{\text{oiss}}$	Output Capacitance			122		pF
$C_{\text{rss}}$	Reverse Transfer Capacitance			8		pF
$C_{\text{o(er)}}$	Effective output capacitance, energy related	$V_{DS} = 0 \dots 400 \text{ V}$ , $V_{GS} = 0 \text{ V}$		180		pF
$C_{\text{o(tr)}}$	Effective output capacitance, time related	$V_{DS} = 0 \dots 400 \text{ V}$ , $V_{GS} = 0 \text{ V}$ , $I_D = \text{constant}$		962		pF
$R_g$	Gate Resistance	$f = 1 \text{ MHz}$		1.7		$\Omega$
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn On Delay Time	$V_{DD} = 400 \text{ V}$ , $I_D = 15 \text{ A}$ , $V_{GS} = 10 \text{ V}$ , $R_G = 3 \Omega$		47		ns
$t_r$	Rise Time			23		ns
$t_{d(off)}$	Turn Off Delay Time			87		ns
$t_f$	Fall Time			2		ns
$Q_g$	Total Gate Charge	$V_{DD} = 400 \text{ V}$ , $I_D = 15 \text{ A}$ , $V_{GS} = 10 \text{ V}$		133		nC
$Q_{gs}$	Gate - Source Charge			31		nC
$Q_{gd}$	Gate - Drain Charge			58		nC
<b>Drain - Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Body - Diode Forward Current			50		A
$I_{\text{SM}}$	Maximum Pulsed Body - Diode Forward Current			200		A
$V_{SD}$	Diode Forward Voltage	$V_{GS} = 0 \text{ V}$ , $I_S = 15 \text{ A}$		0.8		V
$t_{rr}$	Reverse recovery time	$V_{DD} = 400 \text{ V}$ , $I_D = 15 \text{ A}$ , $di/dt = 100 \text{ A}/\mu\text{s}$		192		ns
$Q_{rr}$	Reverse recovery charge			2		$\mu\text{C}$
$I_{\text{rrm}}$	Peak Reverse Recovery Current			20		A

## Electrical Characteristics Diagrams

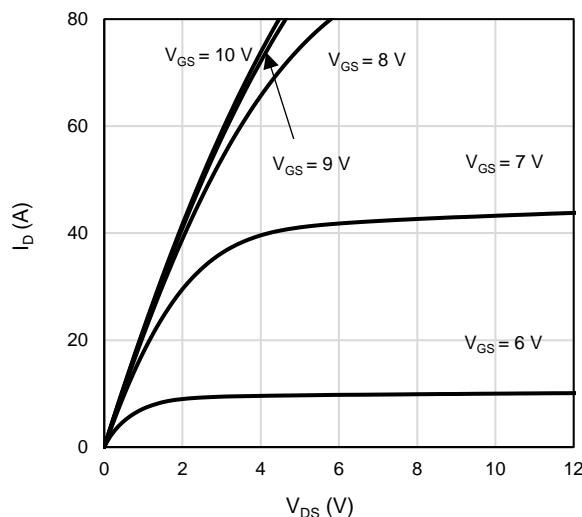


Figure 1: On-Region Characteristics

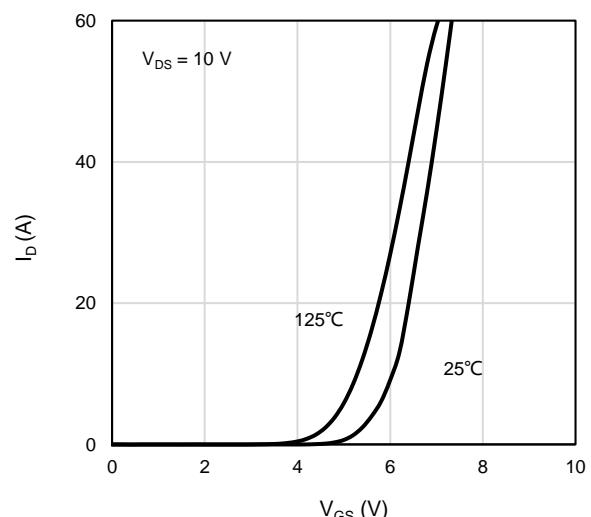


Figure 2: Transfer Characteristics

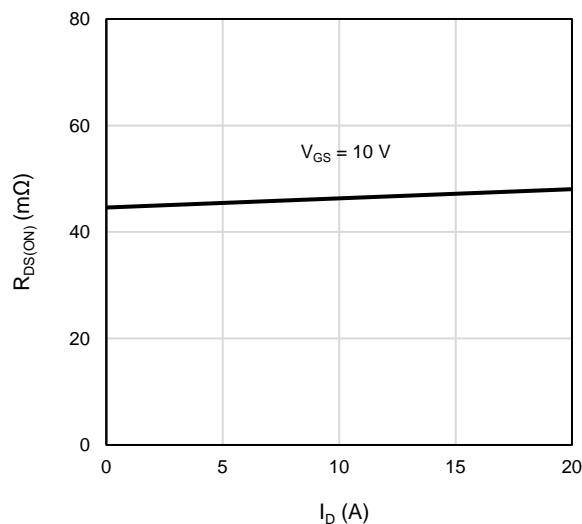


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

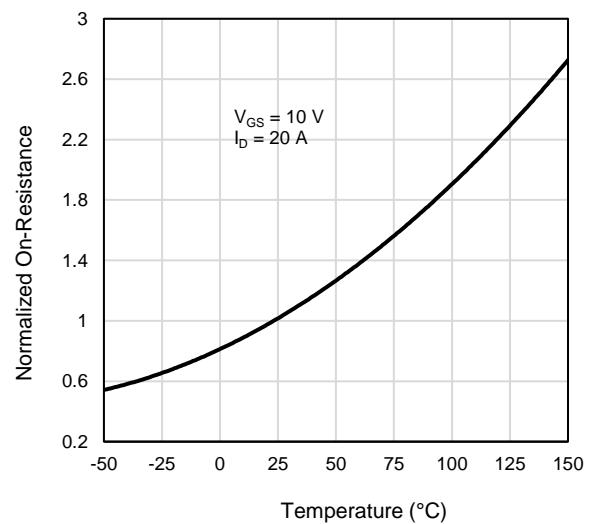


Figure 4: On-Resistance vs. Junction Temperature

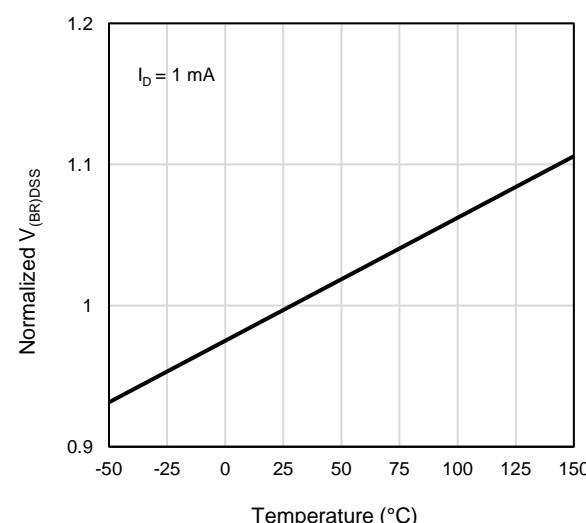


Figure 5: Breakdown Voltage vs. Junction Temperature

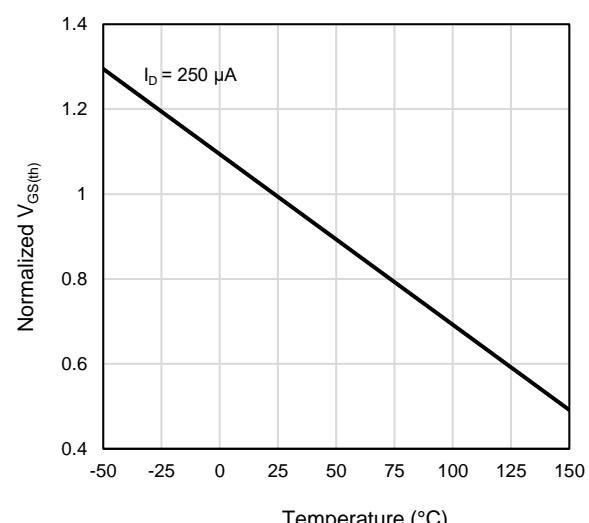


Figure 6: Threshold Voltage vs. Junction Temperature

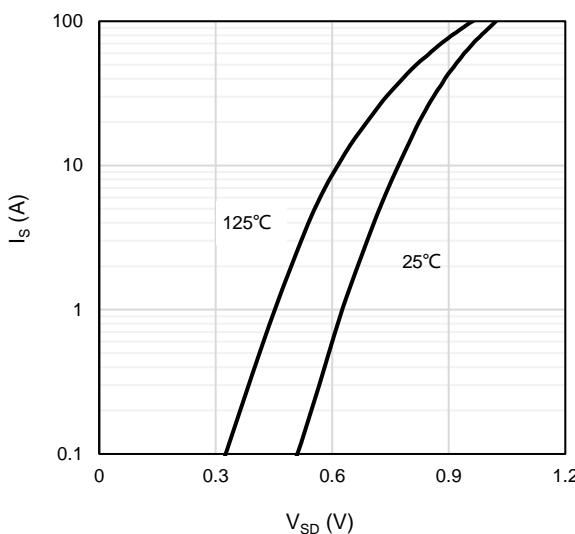


Figure 7: Body-Diode Characteristics

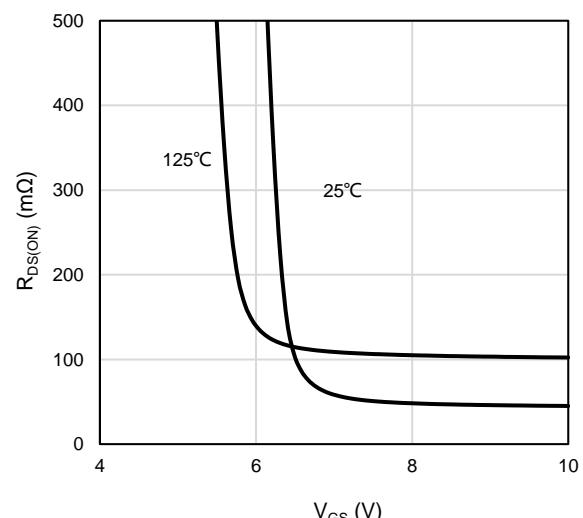


Figure 8: On-Resistance vs. Gate-Source Voltage

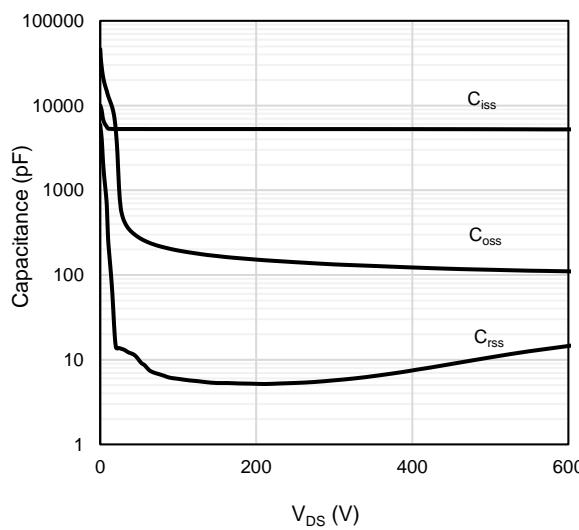


Figure 9: Capacitance Characteristics

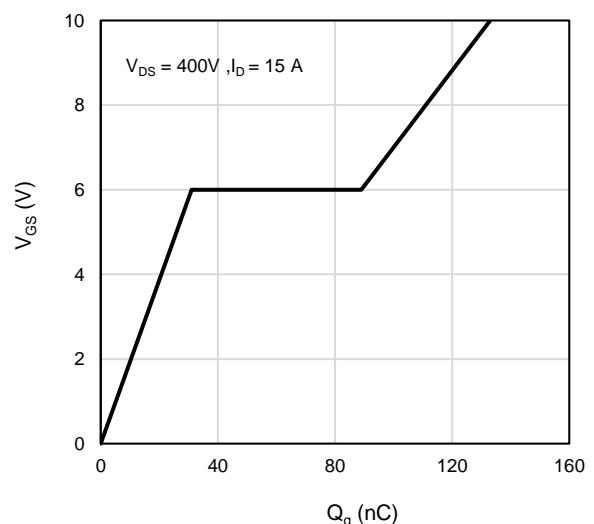


Figure 10: Gate-Charge Characteristics

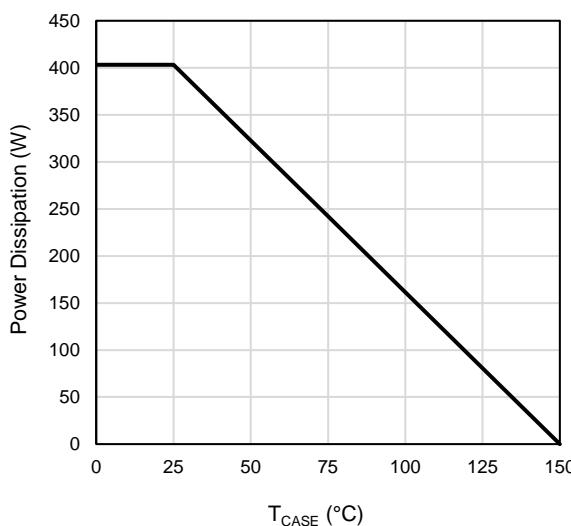


Figure 11: Power De-rating

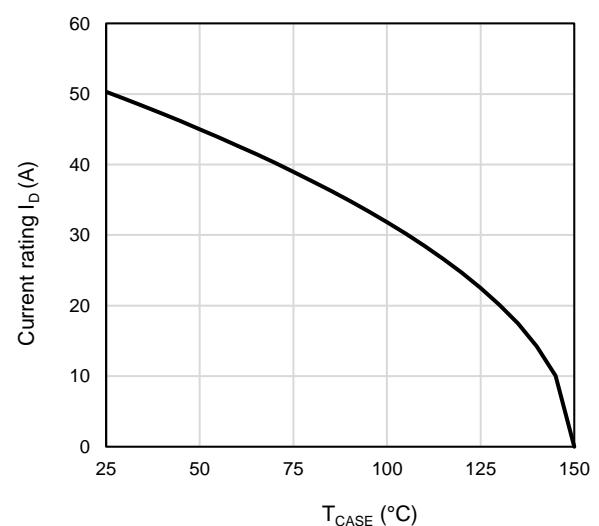


Figure 12: Current De-rating

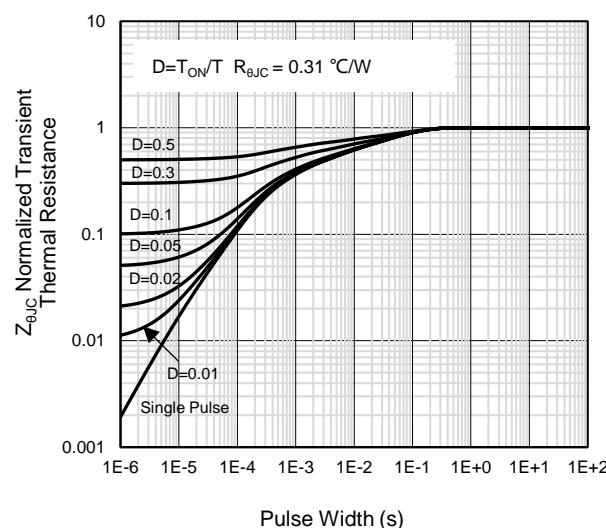


Figure 13: Normalized Maximum Transient Thermal Impedance

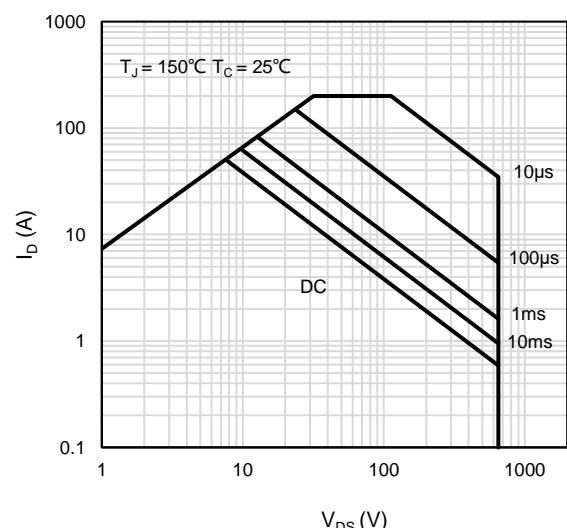
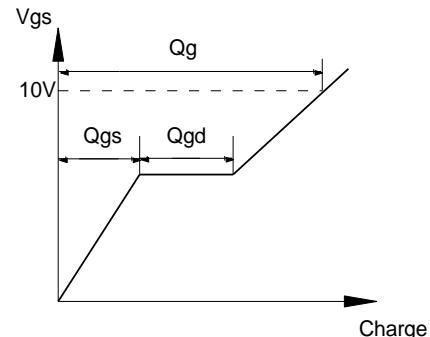
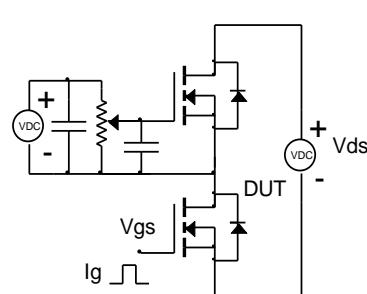


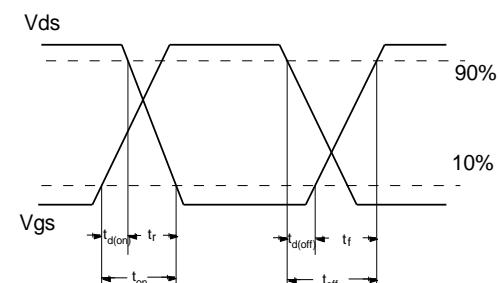
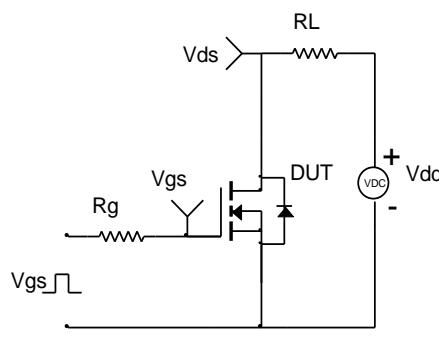
Figure 14: Maximum Forward Biased Safe Operating Area

## Test Circuit and Waveform

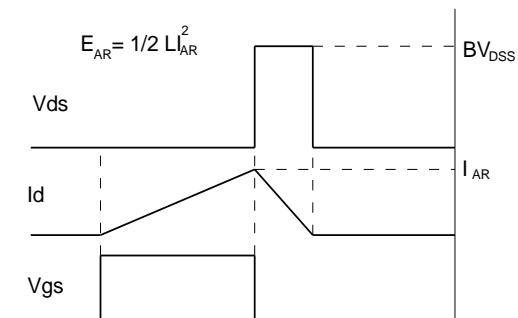
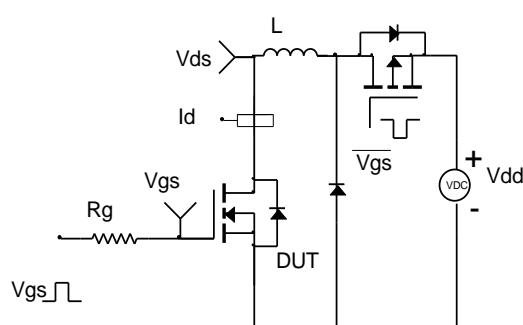
Gate Charge Test Circuit &amp; Waveform



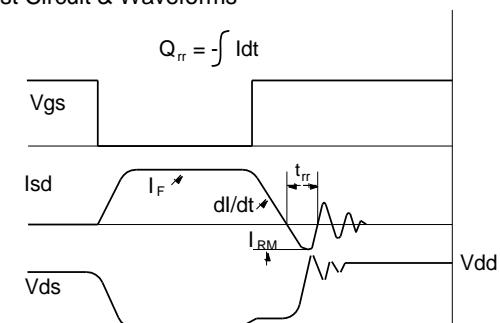
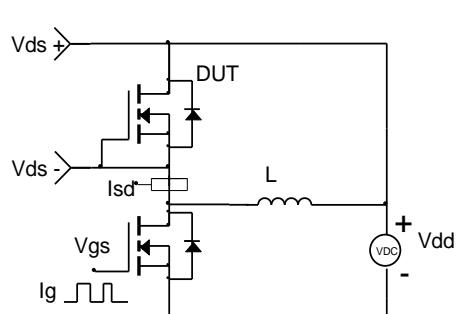
Resistive Switching Test Circuit &amp; Waveforms



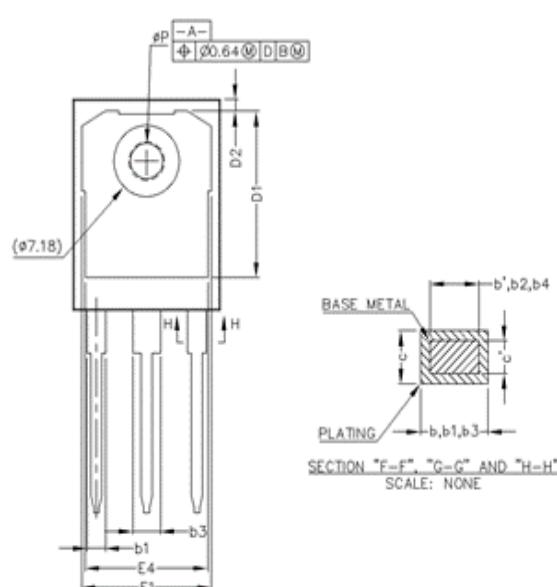
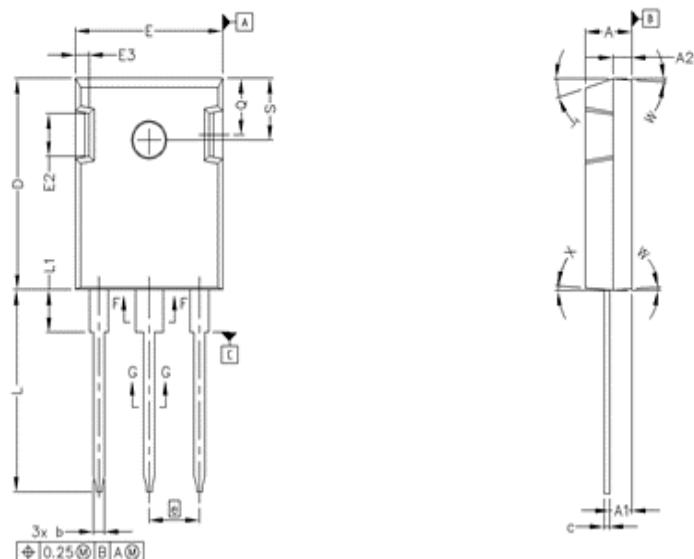
Unclamped Inductive Switching (UIS) Test Circuit &amp; Waveforms



Diode Recovery Test Circuit &amp; Waveforms



## Package Outlines



SYMBOL	MIN	MAX
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	1.91	2.41
b2	1.91	2.16
b3	2.87	3.38
b4	2.87	3.13
c'	0.55	0.65
c	0.55	0.68
D	20.80	21.10
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	5.44 BSC	
N	3	
L	19.81	20.32
L1	4.10	4.40
P	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5 ° REF	
W	3.5 ° REF	
X	4 ° REF	

## Marking Information



2S65N590WMF  
KYWWZZZ

Note:

2S65N590WMF = Product Name Code

KYWWZZZ = Date code

Contact ALKAIDSEMI sales for detail information

## Revision History

Revision	Released	Remark
Rev.1.1	2023	

## Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior